Syllabus

Instructor: Dr. Herman Lam
- Office Benton Hall, Room 313
- Office hours: Mon/Fri 11:30 am – 12:30 pm; Tues/Thurs 2:30 – 3:30 pm
- Telephone: (352) 392-2689
- Email: hlam@ufl.edu

TAs: TA Lab schedule and office hours (All office hours are in NEB 222, unless specified otherwise)
- Abrar Polani: abrarmp@gmail.com
  - Labs: Mon P9 – P11; Tues P9 – P11; Thurs P9 – P11 (P9 means Period 9)
  - Office hour: Mon P8, Tues P8, Thurs P8
- Paymon Abtahi: pabtahi@ufl.edu
  - Labs: Wed E1-E3; Thurs P3 – P5
  - Office hour: Wed P6; Fri P6

Class lectures:
- MWF 4th period (10:40 – 11:30 pm), Larsen Hall 330

Textbook:

Lab board and instrument:
- Altera DE0 board: an FPGA-based laboratory board containing an Altera Cyclone III 3C16 FPGA device. Information on the DE0 Board can be found here, including the DE0 User Manual.
- Digilent Analog Discovery multi-function instrument
- The DE0 Board will be provided to you (included in your lab fee)


VHDL resources:
- Some examples and notes:
  - Introduction to VHDL
  - Some VHDL examples
  - Some example testbenches
Course Objective:
The objective of this course is to study the fundamentals, methodologies, and techniques for the structured design of digital systems, using the state-of-the-art technologies and design environments and tools.

Course Contents:
I. Digital design building blocks and technologies
   - Review of commonly-used digital components: MUXes, deMUXes, decoders, encoders, adders, flip-flops, counters, registers, etc. In addition, we will learn to specify these components in VHDL.
   - Carry-look-ahead adders, ALUs, multipliers
   - Programmable logic devices: PLA's, PROM's, CPLD's, and FPGA's
   - Memories - RAM, SRAM, DRAM, ROM, Flash, HMC

II. Digital design methodology and techniques for finite state machines (FSM) and FPGA's
   - Top-down, modular design
   - Controller/controlled-component architecture
   - ASM fundamentals and design methods
   - Implementation methods - traditional, MUX, ROM, "one-hot"
   - Design and Implementation techniques using FPGA's
   - Testing and design for testing
   - Digital design examples (labs)

III. Design languages, tools, and environments (lab-intensive)
   - Design life cycle using model digital development environments
- Design specification: graphical, VHDL
- Logic synthesis
- Simulation: functional and timing using VHDL testbenches
- Timing analysis
- Device program
- Testing

Honesty Policy:

All students admitted to the University of Florida have signed a statement of academic honesty committing themselves to be honest in all academic work and understanding that failure to comply with this commitment will result in disciplinary action. This statement is a reminder to uphold your obligation as a UF student and to be honest in all work submitted and exams taken in this course and all others.

Accommodation for Students with Disabilities:

Students requesting classroom accommodation must first register with the Dean of Students Office. That office will provide the student with documentation that he/she must provide to the course instructor when requesting accommodation.

UF Counseling Services:

Resources are available on-campus for students having personal problems or lacking clear career and academic goals. The resources include:

- UF Counseling & Wellness Center, 3190 Radio Rd, 392-1575, psychological and psychiatric services.
- Career Resource Center, Reitz Union, 392-1601, career and job search services.

Software Use:

All faculty, staff and student of the University are required and expected to obey the laws and legal agreements governing software use. Failure to do so can lead to monetary damages and/or criminal penalties for the individual violator. Because such violations are also against University policies and rules, disciplinary action will be taken as appropriate. We, the members of the University of Florida community, pledge to uphold ourselves and our peers to the highest standards of honesty and integrity.